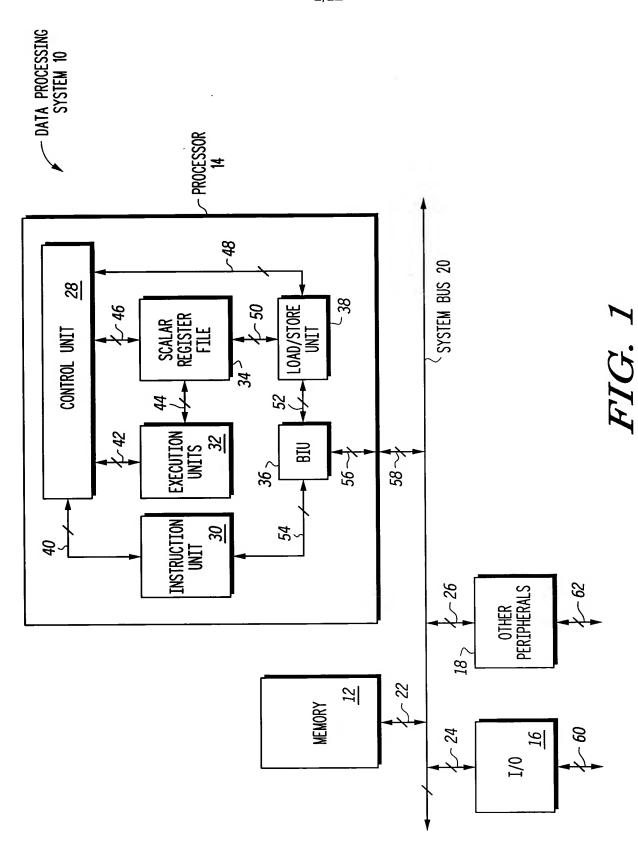


Γ



- 1

Ivex.[s/u].[ms].[ds] rD, rA, rB

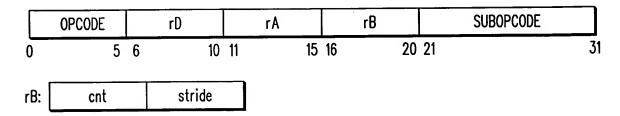


FIG. 2

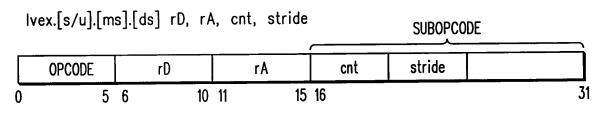


FIG. 3

Imvex.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB: cnt stride skip skip_cnt

FIG. 4

Imvex.[s/u].[ms].[ds] rD, rA, cnt, stride, skip, skip_cnt

OPCODE rD rA cnt stride skip skip_cnt

Imvex2.[s/u].[ms].[ds] rD, rA, rB

Γ

OPCODE	rD	rA	rВ	SUBOPCODE

rB: cnt rcnt stride skip

FIG. 6

lstrmvex.[s/u].[ms].[ds] rD, rA, rB

OPCODE rD rA rB SUBOPCODE

rB: cnt rcnt stride skip skip_cnt

FIG. 7

stvex.[s/u].[ms].[ss].[h/I] rS, rA, rB

OPCODE	rS	rA	rB	SUBOPCODE
--------	----	----	----	-----------

rB: cnt stride

 $stmvex.[s/u].[ms].[ss].[h/I] \ rS, \ rA, \ rB$

 Γ

OPCODE	rS	rA	rB	SUBOPCODE

rB: cnt stride skip skip_cnt

FIG. 9

 $stmvex2.[s/u].[ms].[ss].[h/I] \ rS, \ rA, \ rB$

			-0	CURODCODE
OPCODE	rS	rA	LR	SUBOPCODE

rB: cnt rcnt stride skip

FIG. 10

ststrmvex.[s/u].[ms].[ss].[h/I] rS, rA, rB

OPCODE	rS	rA	rB	SUBOPCODE

rB: cnt rcnt stride skip skip_cnt

Г

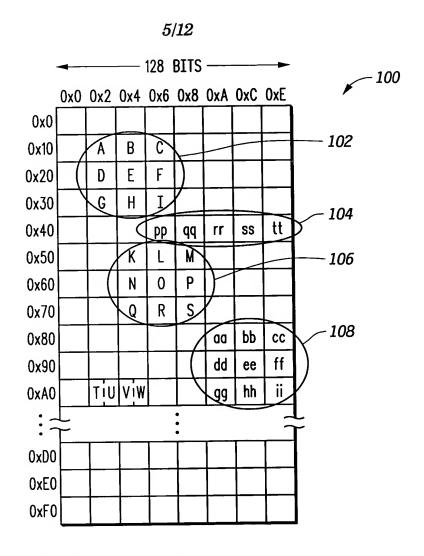
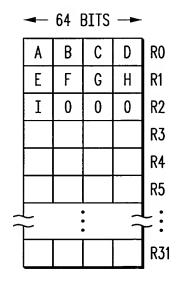


FIG. 12

	← 64 BITS ←					-	64 E	BITS	-		
	Α	В	С	0	R0		D	E	F	0	R0
	К	L	М	0	R1	•	N	0	Р	0	R1
	A+K	B+L	C+M	0	R2		D+N	E+0	F+P	0	R2
					R3		G	Н	I	0	R3
					R4		Q	R	S	0	R4
					R5		G+Q	H+R	I+S	0	R5
-			•	=	L :	2			•	(<u> </u> :
					R31						R31
I	FIG. 13					F	TI	G	Y.	1	4

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Γ

	← 64 BITS →									
	A	В	С	0	R0					
	D	Ε	F	0	R1					
	G	Н	Ι	0	R2					
		-			R3					
	+ !T	÷įU	- iV	0	R4					
					R5					
_))	_:					
					R31					

FIG. 15

FIG. 16

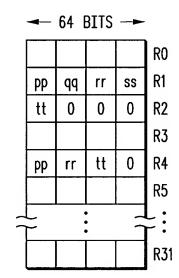
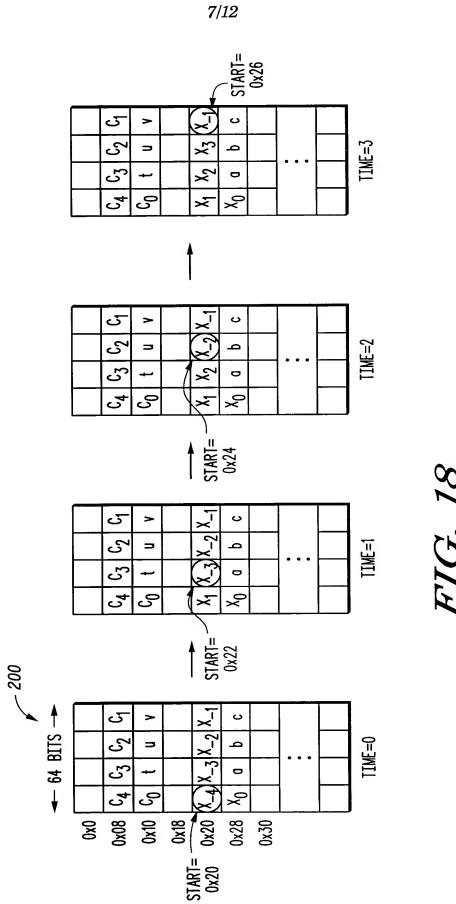


FIG. 17



-	← 64 BITS ←						
	0x	08		R1			
	0x	20		R2			
C ₄	C_4 C_3 C_2 C_1						
c ₀	0	0	co	R7			
X ₋₄	X ₋₃	X_{-2}	X ₋₁	R8			
X ₀	X ₀ 0 0 0						
$C_4 \cdot X_{-4}$	$c_4 \cdot x_{-4} + c_3 \cdot x_{-3} + c_2 \cdot x_{-2} + c_1 \cdot x_{-1}$						
c_0	•X ₀ +0•(0+0-0+0	1-0	R11			

Γ

-	◆ 64 BITS →						
	0x	22		R2			
C ₄	C ₃	c ₂	C ₁	R6			
c ₀	0	0	c ₀	R7			
X ₋₃	X ₋₂	X ₋₁	X ₀ _	R8			
X ₁	X ₁ 0 0 0						
$C_4 \cdot X_{-3} + C_3 \cdot X_{-2} + C_2 \cdot X_{-1} + C_1 \cdot X_0$							
c^0	C ₀ ·X ₁ +0·0+0·0+0·0						

FIG. 19

FIG. 20

~	← 64 BITS ← ►						
		0x	24		R2		
			•				
	C ₄	c_3	c ₂	C ₁	R6		
	c_0	0	0	c ₀	R7		
	X ₋₂	X ₋₁	Х ₀	X ₁	R8		
	X ₂ 0 0 0						
C	$C_4 \cdot X_{-2} + C_3 \cdot X_{-1} + C_2 \cdot X_0 + C_1 \cdot X_1$						
	c_0	•X ₂ +0•(0+0-0+0	-0	R11		

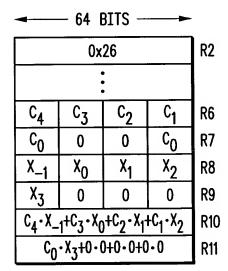


FIG. 21

	64 I	BITS	-			
Α	В	С	0	R1		
K	N	Q	0	R2		
0x12						
0x54						

← 64 BITS ←						
D	E	F	0	R1		
K	N	Q	0	R2		
				R3		
	0x	12		R4		
	0x54					
	:					
A٠	R10					
	R11					
	R12					

FIG. 23

FIG.	24

← 64 BITS ←								
G	G H I O							
K	K N Q O							
			Î	R3				
	0x	12		R4				
	0x54							
	:							
Α.	A·K+B·N+C·Q							
D.	D·K+E·N+F·Q							

← 64 BITS ←							
G	G H I O						
L	L 0 R 0						
				R3			
	0x	12		R4			
	0x54						
	:						
A٠	R10						
D٠	D·K+E·N+F·Q						
G.	R12						

FIG. 25

lmvex_skip_once.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB: cnt stride skip skip_cnt

FIG. 27

 $lmvex_cb.[s/u].[ms].[ds] rD, rA, rB$

OPCODE	rD	rA	rB	SUBOPCODE

rB: buffer_size offset

FIG. 28

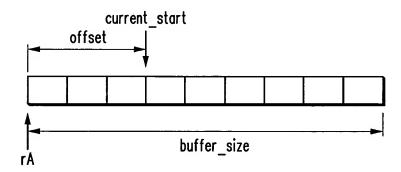


FIG. 29

lstrmvex_cb.[s/u].[ms].[ds] rD, rA, rB

OPCODE	rD	rA	rB	SUBOPCODE

rB: buffer_size offset

 $lmvex_{fft.[s/u].[ms].[ds]}$ rD, rA, rB

	1		<u> </u>	
I OPCODE	l rD	rA	l rB	SUBOPCODE
0.000	, _			0050, 0052

rB: radix

Γ

FIG. 31

 $stmvex_fft.[s/u].[ms].[ss] \ rS, \ rA, \ rB$

ODCODE	_n		-D	CHDODCODE
OPCODE	l ru	r A	LR	SUBOPCODE

rB: radix

FIG. 32

 $lmstrmvex_fft.[s/u].[ms].[ds] rD, rA, rB$

OPCODE	rD	rA	rB	SUBOPCODE
0, 0002				00001 0002

rB: radix

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	0x0							0xE	300
0x0									
0x10				X ₀	X ₁	X_2	X ₃	X ₄	
0x20	X ₅	Х ₆	X ₇	i					
0x30									
0x40			Υ ₀	Y ₄	Y ₆	Y ₂	Y ₁	Y ₅	
0x50	Yz	Y ₇						•	
0x60									

FIG. 34

Х ₀	X ₄	Х ₆	X ₂	R1
Х ₁	X ₅	Х3	X ₇	R2
				R3
Y ₀	Y ₁	Y ₂	Y ₃	R4
Y ₄	Y ₅	Y ₆	Y ₇	R5

FIG. 35